



Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore (MP)													
Choice Based Credit System (CBCS) Scheme													
Bachelor of Technology (Electronics & Communication)													
VIII - SEMESTER(w.e.f. Batch 2018-19)													
Sr. No.	Subject Code	Name of Subject	Course Code	Teaching Scheme/ Week			Examination Scheme					Total Marks	Credits
				L	T	P	Theory			Practical			
							End Sem University Exam	Two Term Exam	Teachers Asses- ment*	End Sem University Exam	Teacher s Asses- ment*		
1	BTEC801	Project / Industrial Internship		0	0	24	0	0	0	300	200	500	12
TOTAL				0	0	24	0	0	0	300	200	500	12
*Either Student has to undergo Industrial Internship or Project.													


Chairperson
Board of Studies
 Shri Vaishnav Vidyapeeth Vishwavidyalaya
 Indore


Chairperson
Faculty of Studies
 Shri Vaishnav Vidyapeeth Vishwavidyalaya, INDORE


Controller of Examinations
 Shri Vaishnav Vidyapeeth Vishwavidyalaya


Registrar
 Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore


Vice Chancellor
 Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore